

# PXIe-LCR5100 Bundle

Expandable PXI bundle based on PXIe-4190 LCR Meter and SMU, 2 MHz, +/- 40 V

**Specifications** 

PXIe-1083 and PXIe-4190



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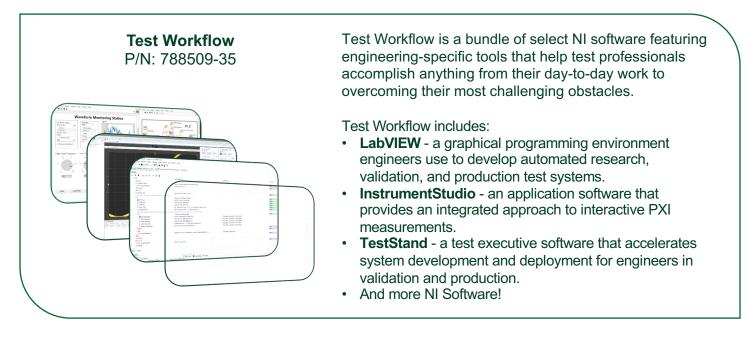
Mess- und Prüftechnik, Die Experten,

## PXI LCR Bundle

#### In the Box



#### **Recommended Software**



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# PXIe-1083 Specifications





## PXIe-1083 Specifications

This document contains specifications for the PXIe-1083 chassis.

## Electrical

The following section provides information about the PXIe-1083 AC input and DC output.

## AC Input

Input rating	100 VAC to 240 VAC, 50 Hz/60 Hz, 6 A to 3 A
Operating voltage range <sup>1</sup>	90 VAC to 264 VAC
Nominal input frequency	50 Hz/60 Hz
Operating frequency range <sup>1</sup>	47 Hz to 63 Hz
Efficiency	78% typical
Over-current protection	Internal fuse in line
Main power disconnect	The AC power cable provides main power disconnect. Do not position the equipment so that it is difficult to disconnect the power cord. The front-panel power switch causes the internal chassis power supply to provide DC power to the PXI Express backplane.



Caution Disconnect power cord to completely remove power.

#### DC Output

#### DC output characteristics of the PXIe-1083.

Voltage Rail	Maximum Current	Load Regulation	Maximum Ripple and Noise (20 MHz BW)
+5V_AUX	1.0 A	±5%	50 mVpp
+12 V	30.1 A	±5%	120 mVpp
+5 V	25.1 A	±5%	50 mVpp
+3.3 V	30.7 A	±5%	50 mVpp
-12 V	0.75 A	±5%	120 mVpp

Maximum total available power for the PXIe-1083 is 293 W.

The maximum combined power available on +3.3 V and +5 V is 180 W.

The maximum power available for each Thunderbolt port is 15 W (5 V/3 A).

#### Table 1. Backplane Slot Current Capacity

Slot	+5 V	V (I/O)	+3.3 V	+12 V	-12 V	5 V <sub>AUX</sub>
Hybrid Peripheral Slot with PXI-5 Peripheral	-	-	3 A	6 A	-	1 A
Hybrid Peripheral Slot with PXI-1 Peripheral	6 A	5 A	6 A	1 A	1 A	-

**Note** PCI V(I/O) pins in Hybrid Peripheral Slots are connected to +5 V.

Note The maximum power dissipated in a peripheral slot should not exceed 58 W. Refer to the **Operating Environment** section for ambient temperature considerations at 58 W.

Over-current protection	All outputs are protected from short circuit and overload, they recover and return to regulation when the overload is removed and the power is cycled.
Over-voltage protection	+3.3 V clamped at 3.7 V to 4.3 V, +5 V clamped at 5.7 V to 6.5 V, +12 V clamped at 13.4 V to 15.6 V

## Chassis Cooling

Module cooling	Forced air circulation (positive pressurization) through one 150 CFM fan
Module slot airflow direction	Bottom of module to top of module
Module intake	Bottom of chassis
Module exhaust	Top, right side of chassis
Slot cooling capacity	58 W; slot 6 supports 58 W cooling with high fan mode
Power supply cooling	Forced air circulation through integrated fans
Power supply intake	Front and left side chassis
Power supply exhaust	Rear of chassis
Minimum chassis cooling cl	earances
Above	44.45 mm (1.75 in.)
Rear	44.45 mm (1.75 in.)
Sides	44.45 mm (1.75 in.)
Below	
Rack	44.45 mm (1.75 in.)
Desktop	25.4 mm (1.00 in.)

## Environmental

Maximum altitude	2,000 m (6,560 ft.), 800 mbar (at 25 °C ambient, high fan mode)
Pollution Degree	2

Indoor use only.

## Operating Environment

Ambient temperature range	
When all peripheral modules	0 °C to 50 °C (IEC 60068-2-1 and IEC 60068-2-2.) <sup>2</sup> Meets
require ≤38 W cooling capacity	MIL-PRF-28800F Class 3 low temperature limit and high
per slot	temperature limit.
When any peripheral module	0 °C to 40 °C (IEC 60068-2-1 and IEC 60068-2-2.) <sup>2</sup> Meets
requires >38 W cooling capacity	MIL-PRF-28800F Class 3 low temperature limit and MIL-
per slot	PRF-28800F Class 4 high temperature limit.
Relative humidity range	20% to 80%, noncondensing

## Storage Environment

Ambient temperature range	–40 °C to 71 °C (IEC-60068-2-1 and IEC-60068-2-2.) <sup>[3]</sup> Meets MIL- PRF-28800F Class 3 limits.
Relative humidity range	10% to 95%, noncondensing

## Shock and Vibration

Operational shock	30 g peak, half-sine, 11 ms pulse (IEC-60068-2-27.) <sup>3</sup> Meets MIL- PRF-28800F Class 2 limits.
Operational random vibration	5 to 500 Hz, 0.3 g <sub>rms</sub>
Non-operating vibration	5 to 500 Hz, 2.4 g <sub>rms</sub> (IEC 60068-2-64.) <sup>3</sup> Non-operating test profile exceeds the requirements of MIL-PRF-28800F, Class 3.

## Acoustic Emissions

#### Sound Pressure Level (at Operator Position)

(Tested in accordance with ISO 7779. Meets MIL-PRF-28800F requirements.)

38 W Profile	
Auto fan (up to 30 °C ambient)	33.7 dBA
High fan	50.8 dBA
58 W Profile	
Auto fan (up to 30 °C ambient)	54.7 dBA
High fan	55.3 dBA

#### Sound Power Level

#### 38 W Profile

Auto fan (up to 30 °C ambient)	44.9 dBA
High fan	60.3 dBA
58 W Profile	
Auto fan (up to 30 °C ambient)	63.4 dBA

Note The protection provided by the PXIe-1083 can be impaired if it is used in a manner not described in this document.

## Safety Compliance Standards

This product is designed to meet the requirements of the following electrical equipment safety standards for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA C22.2 No. 61010-1

Note For safety certifications, refer to the product label or the <u>Product</u> <u>Certifications and Declarations</u> section.

## **EMC Guidelines**

This product was tested and complies with the regulatory requirements and limits for electromagnetic compatibility (EMC) stated in the product specifications. These requirements and limits provide reasonable protection against harmful interference when the product is operated in the intended operational electromagnetic environment. This product is intended for use in industrial locations. However, harmful interference may occur in some installations, when the product is connected to a peripheral device or test object, or if the product is used in residential areas. To minimize interference with radio and television reception and prevent unacceptable performance degradation, install and use this product in strict accordance with the instructions in the product documentation.

Furthermore, any changes or modifications to the product not expressly approved by NI could void your authority to operate it under your local regulatory rules.

#### **EMC** Notices

Refer to the following notices for cables, accessories, and prevention measures necessary to ensure the specified EMC performance.

## Notice

For EMC declarations and certifications, and additional information, refer to the <u>Product Certifications and Declarations</u> section.

**Notice** Changes or modifications to the product not expressly approved by NI could void your authority to operate the product under your local regulatory rules.

Notice Operate this product only with shielded cables and accessories.

#### **Electromagnetic Compatibility Standards**

This product meets the requirements of the following EMC standards for electrical equipment for measurement, control, and laboratory use:

- EN 61326-1 (IEC 61326-1): Class A emissions; Basic immunity
- EN 55011 (CISPR 11): Group 1, Class A emissions

AS/NZS CISPR 11: Group 1, Class A emissions

**Note** Group 1 equipment (per CISPR 11) is any industrial, scientific, or medical equipment that does not intentionally generate radio frequency energy for the treatment of material or inspection/analysis purposes.



**Note** In Europe, Canada, Australia, and New Zealand (per CISPR 11) Class A equipment is intended for use in nonresidential locations.

## CE Compliance $C \in$

This product meets the essential requirements of applicable European Directives, as follows:

- 2014/35/EU; Low-Voltage Directive (safety)
- 2014/30/EU; Electromagnetic Compatibility Directive (EMC)
- 2011/65/EU; Restriction of Hazardous Substances (RoHS)

## **Product Certifications and Declarations**

Refer to the product Declaration of Conformity (DoC) for additional regulatory compliance information. To obtain product certifications and the DoC for NI products, visit <u>ni.com/product-certifications</u>, search by model number, and click the appropriate link.

## **Environmental Management**

NI is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial to the environment and to NI customers.

For additional environmental information, refer to the **Engineering a Healthy Planet** web page at <u>ni.com/environment</u>. This page contains the environmental regulations and directives with which NI complies, as well as other environmental information not included in this document.

#### EU and UK Customers

• A Waste Electrical and Electronic Equipment (WEEE)—At the end of the product life cycle, all NI products must be disposed of according to local laws and regulations. For more information about how to recycle NI products in your region, visit <u>ni.com/environment/weee</u>.

#### 电子信息产品污染控制管理办法(中国 RoHS)

• ◎ ◎ ● 中国 RoHS— NI 符合中国电子信息产品中限制使用某些有害物 质指令(RoHS)。关于 NI 中国 RoHS 合规性信息,请登录 ni.com/environment/ rohs\_china。(For information about China RoHS compliance, go to ni.com/ environment/rohs\_china.)

## Backplane

Size	3U-sized; 5 peripheral slots. Compliant with IEEE 1101.10 mechanical packaging. PXI Express Specification compliant. Accepts both PXI Express and CompactPCI (PICMG 2.0 R 3.0) 3U modules.
Backplane bare-board material	UL 94 V-0 Recognized
Backplane connectors	Conforms to IEC 917 and IEC 1076-4-101, UL 94 V-0 rated

## System Synchronization Clocks

#### 10 MHz System Reference Clock: PXI\_CLK10

Maximum slot-to-slot skew	250 ps
Accuracy	±25 ppm max (guaranteed over the operating temperature range)
Maximum jitter	5 ps RMS phase-jitter (10 Hz–1 MHz range)
Duty-factor	45% to 55%
Unloaded signal swing	3.3 V ±0.3 V



Note For other specifications, refer to the PXI-1 Hardware Specification.

#### 100 MHz System Reference Clock: PXIe\_CLK100 and PXIe\_SYNC100

Maximum slot-to-slot skew	100 ps
Accuracy	±25 ppm max (guaranteed over the operating temperature range)
Maximum jitter	3 ps RMS phase-jitter (10 Hz to 12 kHz range), 2 ps RMS phase-jitter (12 kHz to 20 MHz range)
Duty-factor for PXIe_CLK100	45% to 55%
Absolute differential voltage (When terminated with a 50 Ω load to 1.30 V or Thévenin equivalent)	400 mV to 1000 mV



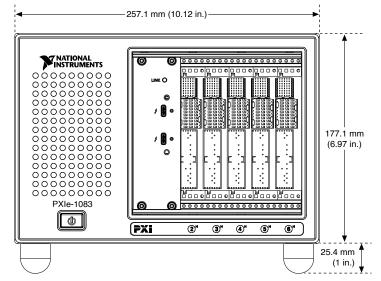
## Note For other specifications, refer to the PXI-5 PXI Express Hardware Specification.

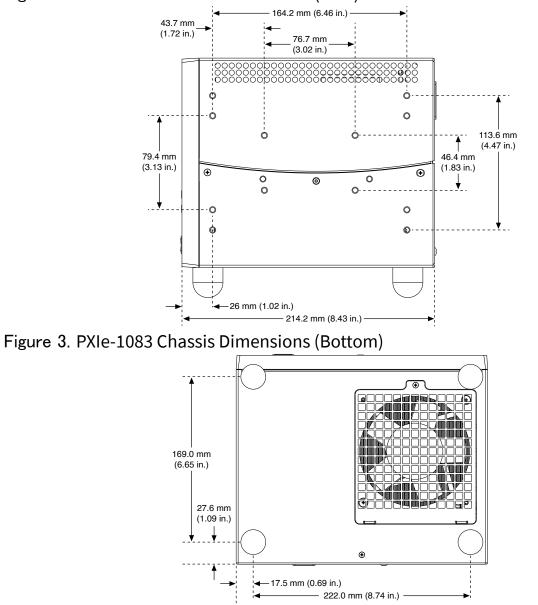
## Mechanical

Standard chassis	dimensions
Height	177.1 mm (6.97 in.)
Width	257.1 mm (10.12 in.)
Depth	214.2 mm (8.43 in.)
Weight	6.7 kg (14.8 lb)
Chassis materials	Extruded Aluminum (6063-T5, 6060-T6), Cold Rolled Steel/Stainless Steel, Santoprene, Urethane Foam, PC-ABS, Nylon, Polyethylene
Finish	Conductive Clear Iridite on Aluminum, Electroplated Nickel on Cold Rolled Steel, Electroplated Zinc on Cold Rolled Steel

The following figures show the PXIe-1083 chassis dimensions. The holes shown are for installing the optional rack mount kits.

#### Figure 1. PXIe-1083 Chassis Dimensions (Front)





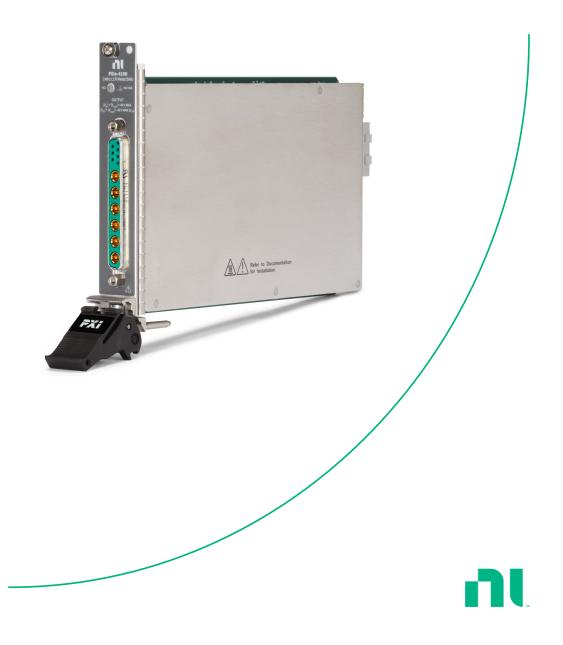
#### Figure 2. PXIe-1083 Chassis Dimensions (Side)

<sup>1</sup> The operating range is guaranteed by design.

<sup>2</sup> This product meets the requirements of the environmental standards for electrical equipment for measurement, control, and laboratory use.

<sup>3</sup> This product meets the requirements of the environmental standards for electrical equipment for measurement, control, and laboratory use.

# PXIe-4190 Specifications



## PXIe-4190 Specifications

## Definitions

**Warranted** specifications describe the performance of a model under stated operating conditions and are covered by the model warranty.

**Characteristics** describe values that are relevant to the use of the model under stated operating conditions but are not covered by the model warranty.

- **Typical** specifications describe the performance met by a majority of models.
- **Nominal** specifications describe an attribute that is based on design, conformance testing, or supplemental testing.

Specifications are **Warranted** unless otherwise noted.

## SMU Specifications

## **SMU Specifications Conditions**

The following conditions must be met when operating in SMU Mode.

- Ambient temperature<sup>[1]</sup> of 23 °C ± 5 °C
- Relative humidity between 10% and 60%, noncondensing
- Chassis with slot cooling capacity ≥58 W
- Calibration interval of 1 year
- 30 minutes warm-up time
- Self-calibration performed within the last 24 hours
- NI-DCPower 21.8 or later installed

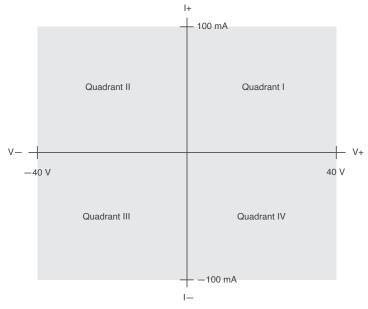
 Connections between force and sense leads are required and must be made while the niDCPower Output Enabled or niDCPower Output Connected properties are set to FALSE<sup>[2]</sup> niDCPower Aperture Time property or
 NIDCPOWER\_ATTR\_APERTURE\_TIME attribute set to 2 power-line cycles (PLC)

 niDCPower Cable Length property or NIDCPOWER\_ATTR\_CABLE\_LENGTH attribute set when using the lower two current ranges

## SMU Instrument Capabilities

DC voltage ranges	1 V	
	10 V	
	40 V	
DC current ranges	1 nA	
	100 nA	
	1 µA	
	10 µA	
	100 µA	
	1 mA	
	10 mA	
	100 mA	
Available DC output power		
Sourcing	4 W	
Sinking	4 W	

#### Figure 1. PXIe-4190 Quadrant Diagram



## SMU Voltage

#### Table 1. Voltage Programming and Measurement Accuracy/Resolution

0	ange Resolution Noise (0.1 Hz to 10 (Noise Limited) Hz, peak-to-peak, typical)	Hz, peak-to-peak,	Accuracy ± (% of Voltage + Offset)		Tempco <sup>[3]</sup> ± (% of Voltage + Offset)/°C
		T <sub>ambient</sub> 23 °C ±5 °C, T <sub>cal</sub> <sup>[4]</sup> ±5 °C			
			% of Voltage	Offset	T <sub>ambient</sub> 0 °C to 30 °C, T <sub>cal</sub> ±5 °C
1 V	100 nV	2 μV	0.009%	160 µV	0.0002% + 1 μV
10 V	1 μV	10 µV	0.008%	1 mV	
40 V	4 μV	50 μV	0.009%	4.1 mV	

## SMU Current

Range	(Noise Limited)	Noise (0.1 Hz to 10 Hz, peak-to- peak, typical)	Accuracy ± (% Offset)	of Current +	Tempco <sup>[5]</sup> ± (% of Current + Offset)/°C
			T <sub>ambient</sub> 23 °C ±5 °C	±5 °C, T <sub>cal</sub> []	
			% of Current	Offset	T <sub>ambient</sub> 0 °C to 30 °C, T <sub>cal</sub> ±5 °C
1 nA <sup>[6]</sup>	1 fA	30 fA	0.14%	2 pA	0.0003% + 20 fA
1 nA <sup>[7]</sup>		60 fA			
100 nA <sup>[6]</sup>	10 fA	300 fA	0.091%	11 pA	
100 nA <sup>[7]</sup>		700 fA			
1 µA	100 fA	2 pA	0.032%	140 pA	
10 µA	1 pA	15 pA	0.026%	1 nA	
100 µA	10 pA	120 pA	0.024%	10 nA	
1 mA	100 pA	1.2 nA	0.023%	100 nA	
10 mA	1 nA	12 nA	0.022%	1 μA	
100 mA	10 nA	120 nA	0.028%	10 µA	

## SMU Noise

Wideband source noise<20 m	V pk-pk, typical <sup>[8]</sup>
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## SMU Remote Sense

Maximum sense lead resistance	200 Ω
Maximum lead drop per lead	1 V

## SMU Guard Output Characteristics

Ca	able guard	
Οι	utput impedance	<100 mΩ, nominal
Of	fset voltage	1 mV, typical

## SMU Measurement and Update Timing

Available sample rates <sup>[9]</sup>	(600 kS/s)/ <b>N</b> , nominal	
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where

- **N** = 1, 2, 3, ...  $2^{24}$
- S is samples

Sample rate accuracy	Equal to PXIe_CLK100 accuracy, nominal
Maximum measure rate to host	600 kS/s, nominal
Input trigger to	
Source event delay	10 μs nominal
Source event jitter	2 μs pk-pk, nominal
Measure event jitter	2 μs pk-pk, nominal

 $\frac{1}{2}$  The ambient temperature of a PXI system is defined as the temperature at the chassis fan inlet (air intake).

<sup>2</sup> Disconnecting the sense leads while both these properties are set to TRUE may result in output protection errors or long settling tails due to the feedback path for the control loop being open. If the PXIe-4190 is run open loop due to accidental sense lead disconnection, allow a minimum of 1 minute after establishing proper lead connections before making measurements.

 $\frac{3}{2}$  Temperature coefficient applies beyond 23 °C ±5 °C ambient within ±5 °C of T<sub>cal</sub>.

 $^4_{-}$  T<sub>cal</sub> is the internal device temperature recorded by the PXIe-4190 at the completion of the last self-calibration

<sup>5</sup>/<sub>2</sub> Temperature coefficient applies beyond 23 °C ±5 °C ambient within ±5 °C T<sub>cal</sub>.

 $\frac{6}{2}$  Under the following additional conditions: with 10 PLC, and 11-point median filter.

<sup>7</sup> Under default specification conditions.

<sup>8</sup> 10 Hz to 20 MHz bandwidth, PXIe-4190 configured for normal transient response.
<sup>9</sup> When source-measuring, both the NI-DCPower Source Delay and Aperture Time properties affect the sampling rate. When taking a measure record, only the Aperture Time property affects the sampling rate.

## LCR Specifications

## LCR Specifications Conditions

The following conditions must be met when operating in LCR Mode.

- Ambient temperature<sup>[1]</sup> of 23 °C ± 5 °C
- Relative humidity between 10% and 60%, noncondensing
- Chassis with slot cooling capacity ≥58 W
- Calibration interval of 1 year
- 30 minutes warm-up time
- Self-calibration performed within the last 24 hours
- NI-DCPower 21.8 or later installed
- Automatic Level Control (ALC) is On
- LCR Measurement Time is Long unless otherwise stated
- Open and short compensation has been completed
- Connections between force and sense leads are required and must be made while the niDCPower Output Enabled or niDCPower Output Connected properties are set to FALSE<sup>[2]</sup>
- Four-terminal pair (4TP) connections to load<sup>[3]</sup>
- niDCPower Cable Length property or NIDCPOWER\_ATTR\_CABLE\_LENGTH attribute set

- Impedance range is within 30% of DUT impedance
- Excitation voltage is set to at least 70 mV RMS
- Temperature is within ±5 °C of last self-calibration
- DC bias is off
- SHDB13W6-4BNCM-LL Cable, 1 m (NI part number 788280-01)

## LCR Instrument Capabilities

Maximum AC voltage		7.07 V RMS
Maximum AC current		70.7 mA RMS
Maximum DC bias voltage range		±40 V, including peak test signal
Maximum DC bias current range		±100 mA, including peak test signal
Measurement times		·
Short	1 ms	
Medium	10 ms	
Long	100 ms	
Custom	User-de	efined

**Note** Measurement times round to the nearest positive integer number of cycles of the stimulus frequency.

## LCR Measurements

- Z—Impedance
- Y—Admittance
- Ls—Inductance using series-equivalent circuit model
- Cs—Capacitance using series-equivalent circuit model
- Rs-Resistance using series-equivalent circuit model
- Lp—Inductance using parallel-equivalent circuit model

- Cp—Capacitance using parallel-equivalent circuit model
- Rp—Resistance using parallel-equivalent circuit model
- D—Dissipation factor
- Q—Quality factor
- V DC—DC voltage measurement
- I DC—DC current measurement
- AC voltage—AC voltage magnitude and phase angle
- AC current—AC current magnitude and phase angle

## LCR Test Signal

Voltage stimulus	
Maximum	7.07 V RMS
Minimum	7.07 mV RMS
Resolution	<1 µV RMS
Maximum current	70.7 mA RMS
Accuracy (ALC on)	
≤10 kHz	±0.2%
10 kHz to 2 MHz	±6%
Current stimulus	
Maximum	70.7 mA RMS
Minimum	707 nA RMS
Resolution	<100 pA RMS
Maximum voltage	7.07 V RMS
Accuracy (ALC on)	
≤10 kHz	±0.2%
10 kHz to 2 MHz	±6%

## LCR DC Bias

Voltage DC bias	
Maximum	±40 V, including peak AC signal
Resolution	<10 µV
Accuracy	0.02% + 5 mV
Current DC bias	
Maximum	±100 mA, including peak AC signal
Resolution	<10 nA
Accuracy	0.04% + 10 μA

## LCR Frequency

Accuracy Equal to PXIe_CLK100 accuracy, nominal
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## LCR Measurement Accuracy

Table 3. Absolute Impedance Magnitude Accuracy, 708 mV RMS to 7.07 V RMS Stimulus Voltage

Impedance	Frequency	/	_	_	_	_	_
Range	40 Hz to	100 Hz to	1 kHz to	10 kHz to	200 kHz to	500 kHz	1 MHz to
	100 Hz	1 kHz	10kHz	200 kHz	500 kHz	to 1 MHz	2 MHz
100 M $\Omega$ to 1 G $\Omega$	1.00%	1.00%			_		
10 M $\Omega$ to 100 M $\Omega$	0.15%	0.15%	0.15%		_		_
$1~\text{M}\Omega$ to $10~\text{M}\Omega$	0.06%	0.06%	0.15%	0.30%			_
100 k $\Omega$ to 1 M $\Omega$	0.05%	0.05%	0.08%	0.30%	0.3%*	0.3% <sup>†</sup>	0.6% <sup>‡</sup>
10 k $\Omega$ to 100 k $\Omega$	0.05%	0.05%	0.08%	0.30%	0.30%	0.30%	0.60%
1 k $\Omega$ to 10 k $\Omega$	0.05%	0.05%	0.08%	0.20%	0.20%	0.20%	0.50%
300 $\Omega$ to 1 k $\Omega$	0.08%	0.08%	0.08%	0.20%	0.20%	0.50%	1.60%

Impedance	Frequency	/	_	_	_	_	_
Range	40 Hz to 100 Hz	100 Hz to 1 kHz	1 kHz to 10kHz	10 kHz to 200 kHz	200 kHz to 500 kHz	500 kHz to 1 MHz	1 MHz to 2 MHz
10 Ω to 300 Ω	0.80%	0.80%	0.80%	0.90%	0.90%	1.20%	2%

Note: When on boundary, use lower adjacent value.

\* Up to 640 k $\Omega$  impedance range.

 $^{\dagger}$  Up to 255 k $\Omega$  impedance range.

<sup>‡</sup> Up to 130 k $\Omega$  impedance range.

Table 4. Absolute Impedance Magnitude Accuracy, 150 mV RMS to 707 mV RMS Stimulus Voltage

Impedance	Frequency						
Range	40 Hz to 100 Hz	100 Hz to 1 kHz	1 kHz to 10kHz	10 kHz to 200 kHz	200 kHz to 500 kHz	500 kHz to 1 MHz	1 MHz to 2 MHz
10 M $\Omega$ to 100 M $\Omega$	0.20%	0.40%	1.10%	—	—	—	_
$1~\text{M}\Omega$ to $10~\text{M}\Omega$	0.06%	0.06%	0.20%	0.90%	—	_	_
100 k $\Omega$ to 1 M $\Omega$	0.05%	0.05%	0.08%	0.90%	0.6%*	0.6%†	0.6% <sup>‡</sup>
10 kΩ to 100 kΩ	0.05%	0.05%	0.08%	0.30%	0.30%	0.30%	0.50%
1 k $\Omega$ to 10 k $\Omega$	0.05%	0.05%	0.08%	0.20%	0.20%	0.20%	0.50%
300 $\Omega$ to 1 k $\Omega$	0.08%	0.08%	0.08%	0.20%	0.20%	0.20%	1.60%
10 $\Omega$ to 300 $\Omega$	0.80%	0.80%	0.80%	0.90%	0.90%	1.20%	2%

Note: When on boundary, use lower adjacent value.

\* Up to 640 k $\Omega$  impedance range.

<sup>†</sup> Up to 255 k $\Omega$  impedance range.

<sup>‡</sup> Up to 130 k $\Omega$  impedance range.

Table 5. Absolute Impedance Magnitude Accuracy Multiplier for Stimuli Below 150 mV RMS

	10 Ω to 300 Ω	$300\Omega$ to $10~\text{M}\Omega$	10 M $\Omega$ to 100 M $\Omega$
50 mV RMS to 150 mV RMS	1	2	3
7.08 mV RMS to 50 mV RMS, typical	2	11	

		10 Ω to 300 Ω	300 Ω to 10 MΩ	10 M $\Omega$ to 100 M $\Omega$
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Note: Absolute accuracy is the **Absolute Impedance Magnitude Accuracy, 150 mV RMS to 707 mV RMS Stimulus Voltage** table value times the respective multiplier.

Table 6. Absolute Impedance Phase Accuracy, 708 mV RMS to 7.07 V RMS Stimulus Voltage

Impedance Range	Frequency	1	_	_	_	_	_
	40 Hz to 100 Hz	100 Hz to 1 kHz	1 kHz to 10 kHz	>10 kHz to 200 kHz	200 kHz to 500 kHz	500 kHz to 1 MHz	1 MHz to 2 MHz
100 M $\Omega$ to 1 G $\Omega$	0.55 °	0.55 °	—	_	_	_	—
10 M $\Omega$ to 100 M $\Omega$	0.19°	0.08 °	0.25 °	_	_	_	
$1~\text{M}\Omega$ to $10~\text{M}\Omega$	0.02 °	0.03 °	0.21°	0.19°	_	_	
100 k $\Omega$ to 1 M $\Omega$	0.01 °	0.02 °	0.19°	0.19°	0.14 °*	0.16 ° <sup>†</sup>	0.26 ° <sup>‡</sup>
10 k $\Omega$ to 100 k $\Omega$	0.01°	0.02 °	0.10 °	0.11°	0.12°	0.13°	0.26 °
1 kΩ to 10 kΩ	0.01°	0.02 °	0.09 °	0.10 °	0.10°	0.12°	0.31°
300 $\Omega$ to 1 k $\Omega$	0.01°	0.03 °	0.12 °	0.08 °	0.13°	0.21°	0.34 °
10 $\Omega$ to 300 $\Omega$	0.01°	0.03 °	0.13 °	0.08 °	0.09 °	0.11°	0.15 °

Note: When on boundary, use lower adjacent value.

\* Up to 640 k $\Omega$  impedance range.

<sup>†</sup> Up to 255 k $\Omega$  impedance range.

<sup>‡</sup> Up to 130 k $\Omega$  impedance range.

Table 7. Absolute Impedance Phase Accuracy, 150 mV RMS to 707 mV RMS Stimulus	
Voltage	

Impedance Range	Frequency						
	40 Hz to 100 Hz	100 Hz to 1 kHz	1 kHz to 10kHz	>10 kHz to 200 kHz	200 kHz to 500 kHz	500 kHz to 1 MHz	1 MHz to 2 MHz
10 M $\Omega$ to 100 M $\Omega$	0.14 °	0.30 °	0.50 °	—	_	—	—
$1~\text{M}\Omega$ to $10~\text{M}\Omega$	0.03 °	0.03 °	0.14 °	0.45 °	_	_	_
100 k $\Omega$ to 1 M $\Omega$	0.02 °	0.03 °	0.14 °	0.45 °	0.22 °*	0.22 °†	0.34 ° <sup>‡</sup>
10 k $\Omega$ to 100 k $\Omega$	0.01°	0.02 °	0.07 °	0.15 °	0.14 °	0.14 °	0.34 °

Impedance Range	Frequency	Frequency			_		
	40 Hz to 100 Hz	100 Hz to 1 kHz	1 kHz to 10kHz	>10 kHz to 200 kHz	200 kHz to 500 kHz	500 kHz to 1 MHz	1 MHz to 2 MHz
1 kΩ to 10 kΩ	0.01°	0.02 °	0.07 °	0.15°	0.09°	0.11°	0.20 °
300 $\Omega$ to 1 k $\Omega$	0.01°	0.02 °	0.07 °	0.08 °	0.09°	0.12°	0.34 °
10 Ω to 300 Ω	0.01°	0.04 °	0.22 °	0.08 °	0.10 °	0.13 °	0.18 °

Note: When on boundary, use lower adjacent value.

\* Up to 640 k $\Omega$  impedance range.

<sup>†</sup> Up to 255 k $\Omega$  impedance range.

<sup>‡</sup> Up to 130 k $\Omega$  impedance range.

Table 8. Absolute Impedance Phase Accuracy Multiplier for Stimuli Below 150 mV RMS

_	10 Ω to 300 Ω	300 Ω to 1 kΩ	1 kΩ to 10 kΩ	>10 kΩ to 100 kΩ	100 kΩ to 1 MΩ	1 MΩ to 10 MΩ	10 MΩ to 100 MΩ
50 mV RMS to 150 mV RMS	2	2	2	2	2	2	3
7.08 mV RMS to 50 mV RMS, typical	20	70	25	25	10	8	8

Note: Absolute accuracy is the **Absolute Impedance Phase Accuracy, 150 mV RMS to 707 mV RMS Stimulus Voltage** table value times the respective multiplier.

<sup>1</sup> The ambient temperature of a PXI system is defined as the temperature at the chassis fan inlet (air intake).

<sup>2</sup> Disconnecting the sense leads while both these properties are set to TRUE may result in output protection errors or long settling tails due to the feedback path for the control loop being open. If the PXIe-4190 is run open loop due to accidental sense lead disconnection, allow a minimum of 1 minute after establishing proper lead connections before making measurements.

 $\frac{3}{2}$  Refer to the **PXIe-4190 Getting Started** for more information on 4TP connections.

## General Specifications

## Isolation

Isolation voltage, any pin to earth ground 40 V DC, Measurement Category I, functional

Note Pins are functionally isolated from chassis ground to prevent ground loops, but do not meet IEC 61010-1 for safety isolation.

Note The PXIe-4190 contains an internal switch controlled by the niDCPower Isolation State property or the NIDCPOWER\_ATTR\_ISOLATION\_STATE attribute that can connect the GUARD terminal to chassis ground and prevent the module output from floating. Isolation ratings only apply when this property/attribute is set to Isolated.

## Protection

Absolute maximum volt	age	
Output HI/Output LO/Sen	nse HI/Sense LO to Output HI/Output LO/Sense HI/Sense LO	±42 V
Output HI/Sense HI to GU	IARD/Isolated Shield	±6V
GUARD/Isolated Shield to	Chassis GND	±42 V
Absolute maximum curr	rent	
All terminals	±150 mA	
Output channel protecti	ion	
Output HI to GUARD/Iso	lated Shield	
Overvoltage	Automatic output disable	
Output LO to all termina	als	
•		

Overcurrent	Automatic output disable
Sense HI/Sense LO to all Overcurrent Current lim	<b>l terminals</b> iter protects inputs up to absolute maximum voltage specification
Overtemperature	Automatic output disable

## **Physical Characteristics**

Dimensions	3U, one-slot, PXI Express/CompactPCI Express module 2.0 cm x 13.0 cm x 21.6 cm (0.8 in. x 5.1 in. x 8.5 in.)
Weight	481 g (17.1 oz)
Front panel connectors	Custom 6 coaxial socket, 7 standard socket, mixed layout D-SUB, female

## Triggers

Input triggers		
Types	Start	
	Source	
	Sequence Advance	
	Measure	
Sources (PXI trigger lines 0 to	7)	
Polarity	Active high (not configurable)	
Minimum pulse width	100 ns	
Destinations <sup>[2]</sup> (PXI trigger lir	nes 0 to 7)	
Polarity	Active high (not configurable)	
Minimum pulse width	200 ns	
Output triggers (events)		

Types S	Source Complete
S	sequence Iteration Complete
S	equence Engine Done
N	leasure Complete
Destinations (PXI trigg	ger lines 0 to 7)[3]
Polarity	Active high (not configurable)
Pulse width	230 ns

## **Calibration Interval**

Recommended calibration interval

1 year

## Power Requirements

+3.3 V	1.0 A
+12 V	2.7 A

## **Environmental Characteristics**

Temperature		
Operating	0 °C to 55 °C [4]	
Storage	-40 °C to 71 °C	
Humidity		
Operating	10% RH to 90% RH, noncondensing [5]	
Storage	5% RH to 95% RH, noncondensing	
Pollution Degree	2	
Maximum altitude	2,000 m (800 mbar) (at 25 °C ambient temperature)	

Shock and Vibration	
Operating vibration	5 Hz to 500 Hz, 0.3 g RMS
Non-operating vibration	5 Hz to 500 Hz, 2.4 g RMS
Operating shock	30 g, half-sine, 11 ms pulse

#### <sup>1</sup> Pulse widths and logic levels are compliant with **PXI Express Hardware Specification Revision 1.0 ECN 1**.

<sup>2</sup> Input triggers can come from any source (PXI trigger or software trigger) and be exported to any PXI trigger line. This allows for easier multi-board synchronization regardless of the trigger source.

<sup>3</sup> Pulse widths and logic levels are compliant with **PXI Express Hardware Specification Revision 1.0 ECN 1**.

<sup>4</sup> Not all chassis can achieve this ambient temperature range. Refer to PXI chassis specifications to determine the ambient temperature ranges your chassis can achieve.

<sup>5</sup> Accuracy specifications are only warranted for operating environments with temperatures below 30 °C and relative humidity levels below 60%. When transitioning the product from a storage or operating environment with relative humidity above 60%, you should allow the product to stabilize in the lower humidity environment for several hours before using it.

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