
PXIe-4081

Specifications



Mess- und Prüftechnik. Die Experten.

**Ihr Ansprechpartner /
Your Partner:**

dataTec AG
E-Mail: info@datatec.eu
datatec.eu



Authorized
Distributor

2025-04-23



Contents

PXIe-4081 Specifications 3

PXIe-4081 Specifications

PXIe-4081 Specifications

These specifications apply to the PXIe-4081, a PXIe, 7½-Digit, $\pm 1,000$ V, Onboard 1.8 MS/s Isolated Digitizer, PXI Digital Multimeter.

Definitions

Warranted specifications describe the performance of a model under stated operating conditions and are covered by the model warranty.

Characteristics describe values that are relevant to the use of the model under stated operating conditions but are not covered by the model warranty.

- *Typical* specifications describe the performance met by a majority of models.
- *Nominal* specifications describe an attribute that is based on design, conformance testing, or supplemental testing.

Specifications are *Warranted* unless otherwise noted.

T_{extcal} is the device temperature at last external calibration.

T_{selfcal} is the device temperature at last self-calibration.

Conditions

Specifications are valid under the following conditions unless otherwise noted. Refer to each section for additional conditions that apply.

- Self-calibration performed within the last 24 hours
- Calibration interval of 2 years
- Warm-up time of 60 minutes
- Resolution set to 6.5 digits or higher for specifications requiring an aperture greater than or equal to 100 ms

PXIe-4081 Pinout

Figure 1. PXIe-4081 Connector Pinout

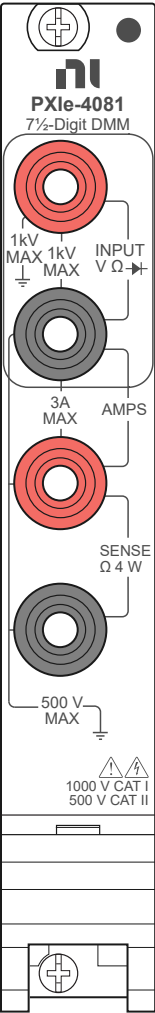


Table 1. Signal Descriptions

Signal Name	Description
Input HI	High-input impedance terminal with a positive polarity defined as Input HI > Input LO. Use this terminal in conjunction with Input LO to measure voltage, 2-wire resistance, and sourcing a test current in 4-wire resistance mode.
Input LO	Low-input impedance terminal that must be connected for all measurement types. Use this terminal in conjunction with Input HI to measure voltage, 2-wire resistance, and sourcing a test current in 4-wire resistance mode. Use this

Signal Name	Description
	terminal in conjunction with Sense HI to measure current.
Sense HI	Positive input terminal when measuring current. Positive current is defined as current flowing into the Sense HI terminal and out of the Input LO terminal. Sense HI is also a high-input impedance terminal when measuring 4-wire resistance. In this mode, connect the terminal to Input HI as close to the DUT as possible to avoid measuring parasitic resistance in the cabling and fixturing.
Sense LO	High-input impedance terminal when measuring 4-wire resistance. In this mode, connect the terminal to Input LO as close to the DUT as possible to avoid measuring parasitic resistance in the cabling and fixturing.

DC Voltage Specifications

DC Voltage Accuracy Specifications

All DC voltage accuracy specifications apply to apertures of ≥ 100 ms, with Auto Zero and ADC calibration enabled. Assumes offset nulling. Otherwise, add 2 μV to the specifications.

Table 2. DC Voltage $\pm$ (ppm of reading + ppm of range)

Range	Input resistance ¹	24 hour ² $T_{\text{selfcal}} \pm 1^\circ\text{C}$	90 day $T_{\text{selfcal}} \pm 5^\circ\text{C}$	2 year $T_{\text{selfcal}} \pm 5^\circ\text{C}$	Tempco/ $^\circ\text{C}^3$	
					Without Self-Cal	With Self-Cal
100 mV	10 M $\Omega \pm 2\%$,	6 + 5	27 + 7	28 + 8	3 + 2	0.3 + 1
1 V	>10 G Ω	4.5 + 0.8	15 + 2.5	18 + 2.5	2 + 0.2	0.3 + 0.1

1. In parallel with 90 pF, typical.
2. Relative to external calibration source.
3. Accuracy specifications allow for the indicated temperature variation. If the device temperature falls outside of that bounds, apply the Tempco 'Without Self-Cal'. Tempco 'With Self-Cal' describes the stability of the calibration mechanism, and is included for reference.

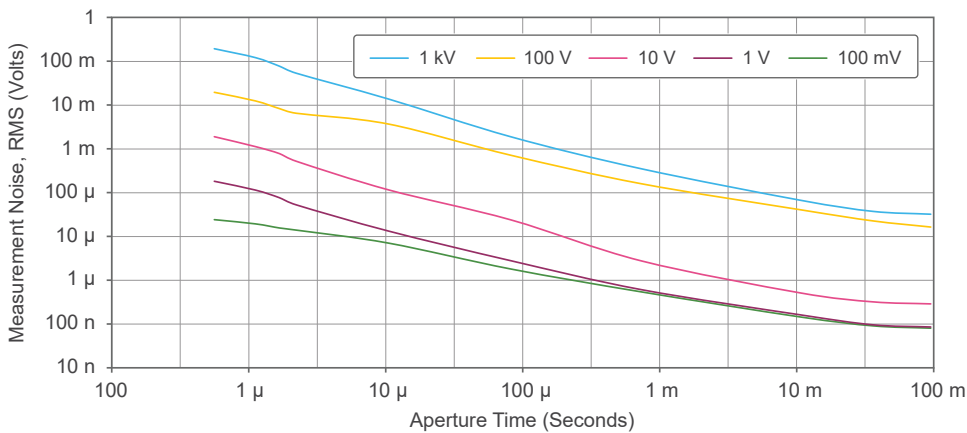
Range	Input resistance	24 hour $T_{\text{selfcal}} \pm 1^\circ\text{C}$	90 day $T_{\text{selfcal}} \pm 5^\circ\text{C}$	2 year $T_{\text{selfcal}} \pm 5^\circ\text{C}$	Tempco/ $^\circ\text{C}$	
					Without Self-Cal	With Self-Cal
10 V	10 M $\Omega \pm 2\%$	2 + 0.5	10.5 + 0.5	12 + 0.5	0.3 + 0.02	0.3 + 0.01
100 V		6 + 2	24 + 2.5	26 + 2.5	4 + 0.2	0.3 + 0.1
1000 V ⁴		4 + 0.5	24 + 0.5	25 + 0.5	3 + 0.02	0.3 + 0.01

DC Voltage Noise



Note The following graph represents DC voltage noise with input shorted, Normal DC Noise Rejection, and Auto Zero ON. For apertures less than 100 ms, add five times the typical rms noise to the accuracy specification.

Figure 2. DC Voltage Noise, Typical



DC Voltage General Specifications

ADC linearity	0.5 ppm of reading + 0.5 ppm of range
Effective common-mode rejection ratio (CMRR) (1 k Ω resistance in LO lead)	>140 dB (DC), 100 ms aperture; >170 dB (>46 Hz) with high-order DC noise rejection, 100 ms aperture, typical
Overrange	105% of range except 1000 V
DC voltage input bias current	<30 pA at 23 $^\circ\text{C}$, typical

- To account for self-heating effects, add 14 μV to the specification for each volt beyond $\pm 300\text{ V}$.

Resistance Specifications

Resistance Accuracy Specifications

All resistance accuracy specifications apply to apertures of ≥ 100 ms, with Offset Compensated Ohms (for ranges ≤ 10 k Ω) or Auto Zero (for ranges ≥ 100 k Ω) and ADC calibration enabled.

Table 3. Resistance (4-Wire and 2-Wire) $\pm$ (ppm of reading + ppm of range)

Range	Test current ⁵	Max test voltage ⁶	Open circuit voltage ⁷	24 hour ⁸ T _{selfcal} ± 1 °C	90 day	2 year	Tempco/°C ⁹		2 Year ¹⁰ T _{selfcal} ± 5 °C
					T _{extcal} ± 10 °C, T _{selfcal} ± 5 °C	T _{extcal} ± 10 °C, T _{selfcal} ± 5 °C	Without Self-Cal	With Self-Cal	
100 Ω	1 mA	100 mV	11.3	9 + 5	40 + 12	55 + 12	5 + 0.12	0.8 + 0.12	60 + 12
1 kΩ	1 mA	1 V	11.3	7 + 0.5	30 + 1.5	45 + 1.5	5 + 0.05	0.8 + 0.05	50 + 1.5
10 kΩ	100 μA	1 V	11.7	7 + 0.5	30 + 1.5	45 + 1.5	5 + 0.05	0.8 + 0.05	50 + 1.5
100 kΩ ¹¹	10 μA	1 V	11.7	7 + 1	36 + 2.5	45 + 2.5	5 + 0.2	2 + 0.2	95 + 2.5
1 MΩ	10 μA	10 V	11.7	6 + 1	60 + 1	60 + 1	5 + 0.05	2 + 0.05	95 + 1
10 MΩ	1 μA	10 V	11.6	60 + 2	130 + 10	130 + 10	20 + 1	20 + 1	800 + 10
100 MΩ ¹²	1 μA 10 MΩ	10 V	9.7	500+ 6	2600 + 10	3000 + 10	300 + 6	300 + 6	—
5 GΩ ¹³ (typical)	1 μA 10 MΩ	10 V	9.7	1% + 0.2%	5% + 0.2%	5% + 0.2%	0.5% + 0.2%	0.5% + 0.2%	—



Note For 2-wire resistance measurements, perform offset nulling or add 200 m Ω to reading.

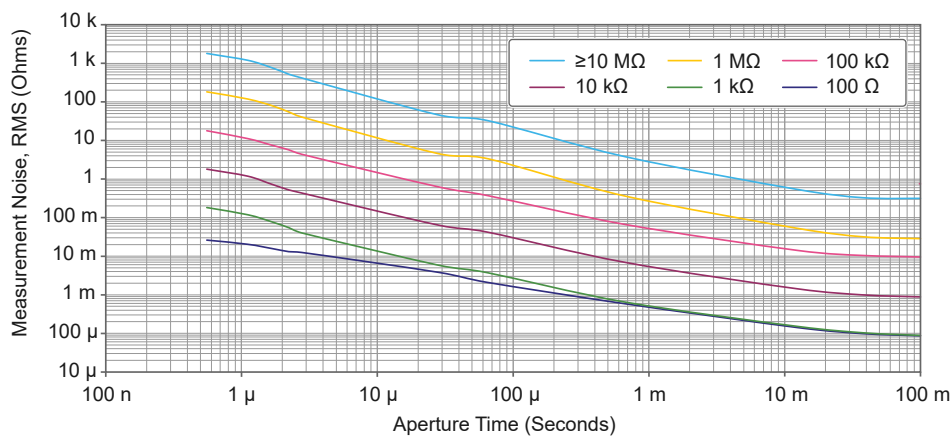
5. -10% to 0% tolerance, typical.
6. Highest nominal voltage present with highest range resistance applied.
7. Nominal voltage present at output with no resistance load.
8. Relative to external calibration source.
9. Accuracy specifications allow for the indicated temperature variation. If the device temperature falls outside of that bounds, apply the Tempco 'Without Self-Cal'. Tempco 'With Self-Cal' describes the stability of the calibration mechanism, and is included for reference.
10. Over full operating temperature range.
11. Perform offset nulling or add 2 ppm of range to the specifications.
12. 2-wire resistance measurement only.
13. 2-wire resistance measurement only.

Resistance Noise



Note The following graph represents resistance noise with input shorted, Normal DC Noise Rejection, and Auto Zero ON. For apertures less than 100 ms, add five times the typical rms noise to the accuracy specification.

Figure 3. PXIe-4081 Resistance Noise, Typical



Resistance General Specifications

Maximum 4-wire lead resistance	Use the lesser of 10% of range or 1 k Ω
--------------------------------	--

DC Current Specifications

DC Current Accuracy Specifications

All DC current accuracy specifications apply for apertures ≥ 100 ms, with Auto Zero and ADC calibration enabled.

Table 4. DC Current $\pm$ (ppm of reading + ppm of range)

Range	Burden voltage, typical	24 hour ¹⁴ $T_{\text{selfcal}} \pm 1^\circ\text{C}$	90 day $T_{\text{extcal}} \pm 10^\circ\text{C}$, $T_{\text{selfcal}} \pm 5^\circ\text{C}$	2 year $T_{\text{extcal}} \pm 10^\circ\text{C}$, $T_{\text{selfcal}} \pm 5^\circ\text{C}$	Tempco/ $^\circ\text{C}$	2 year ¹⁵ $T_{\text{selfcal}} \pm 5^\circ\text{C}$
1 μA ¹⁶	<55 mV	30 + 20	340 + 40	350 + 40	10 + 5	575 + 140
10 μA ¹⁷	<550 mV	30 + 2	140 + 15	200 + 15	10 + 1	500 + 20
100 μA	<60 mV	10 + 10	105 + 20	175 + 20	5 + 0.2	220 + 20
1 mA	<60 mV	13 + 10	100 + 20	170 + 20	5 + 0.2	220 + 20
10 mA	<60 mV	15 + 10	100 + 20	170 + 20	5 + 0.2	250 + 20
100 mA	<100 mV	18 + 10	175 + 20	180 + 20	10 + 0.2	250 + 20
1 A ¹⁸	<250 mV	25 + 10	275 + 20	350 + 20	16 + 0.2	800 + 20
3 A ¹⁹	<700 mV	25 + 5	250 + 20	350 + 20	16 + 0.2	800 + 20

DC Current Noise



Note The following graph represents DC current noise with input open, Normal DC Noise Rejection, and Auto Zero ON. For apertures less than 100 ms, add five times the typical rms noise to accuracy specification.

14. Relative to external calibration source.

15. Over full operating temperature range.

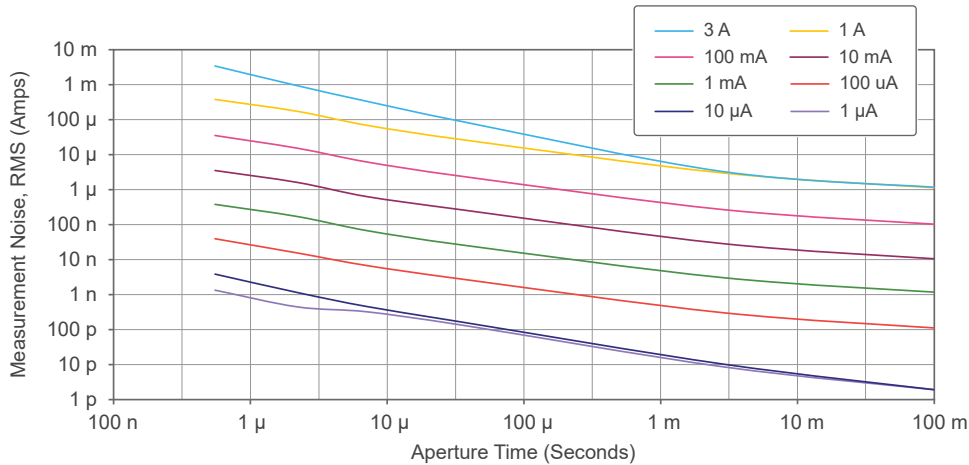
16. 90 day and 2 year specifications are typical.

17. 90 day and 2 year specifications are typical.

18. To account for self-heating effects, for currents larger than 500 mA, add
/ x 75 ppm of reading to the specification.

19. To account for self-heating effects, for currents larger than 500 mA, add / x 75 ppm of reading to the specification.

Figure 4. PXle-4081 DC Current Noise, Typical



DC Current General Specifications

Overrange	105% of range except 1 A range
-----------	--------------------------------

AC Voltage Specifications

AC Voltage Accuracy Specifications



Note Measurement aperture greater than $4/f_L$ where f_L is the lowest frequency component of the signal being measured. Signal amplitudes greater than 1% of range.

Table 5. AC Voltage Accuracy $\pm$ (% of reading + % of range), 2 Years, $T_{\text{extcal}} \pm 10^\circ\text{C}$

Range (RMS)	Peak voltage	1 Hz to 40 Hz ²⁰	>40 Hz to 20 kHz	>20 kHz to 50 kHz	>50 kHz to 100 kHz	>100 kHz to 300 kHz
50 mV ²¹	± 105 mV	$0.1 + 0.02$	$0.05 + 0.02$	$0.07 + 0.02$	$0.3 + 0.02$	$0.7 + 0.15$
500 mV	± 1.05 V	$0.1 + 0.005$	$0.05 + 0.005$	$0.06 + 0.01$	$0.2 + 0.01$	$0.7 + 0.15$
5 V	± 10.5 V					
50 V	± 105 V	$0.1 + 0.005$	$0.12 + 0.05$	$0.6 + 0.05$	$3 + 0.15$	$3 + 0.15$
700 V	± 1000 V					

20. Applies to DC coupled only.

21. Applies to signals >1 mVrms

Table 6. AC Voltage Tempco/ $^{\circ}\text{C} \pm$ (% of reading + % of range)

Range (RMS)	1 Hz to 40 Hz	>40 Hz to 20 kHz	>20 kHz to 50 kHz	>50 kHz to 100 kHz	>100 kHz to 300 kHz
50 mV	0.001 + 0.0002	0.001 + 0.0002	0.001 + 0.001	0.002 + 0.001	0.02 + 0.01
500 mV					
5 V					
50 V	0.001 + 0.0002	0.012 + 0.001	0.045 + 0.001	0.1 + 0.01	0.1 + 0.01
700 V					

AC Voltage General Specifications

Input impedance	10 M Ω $\pm$ 2% in parallel with 90 pF, typical
Input coupling	AC or DC coupled
Overrange	105% of range except 700 V
Maximum Volt-Hertz product	Verified to 2.2×10^7 V-Hz
Maximum DC voltage component	400 V
Common mode rejection ratio (CMRR), 1 k Ω resistance in LO lead	>70 dB (DC to 60 Hz), typical

AC Current Specifications

AC Current Accuracy Specifications



Note Measurement aperture greater than $4/f_L$, where f_L is the lowest frequency component of the signal being measured. Signal amplitudes greater than 1% of range.

Table 7. AC Current Specifications $\pm$ (% of reading + % of range), 2 Years, $T_{\text{extcal}} \pm 10^\circ\text{C}$

Range (RMS)	Peak current	Burden voltage (RMS at 1 kHz), typical	1 Hz to 1 kHz	>1 kHz to 5 kHz	5 kHz to 10 kHz ²²	10 kHz to 20 kHz ²³	Tempco/ $^\circ\text{C}$
100 μA ²⁴	$\pm 200 \mu\text{A}$	<60 mV	0.065 + 0.02	—	—	—	0.002 + 0.0002
1 mA	$\pm 2 \text{ mA}$	<60 mV	0.035 + 0.02	0.06 + 0.02	0.19 + 0.02	0.44 + 0.02	0.001 + 0.0001
10 mA	$\pm 20 \text{ mA}$	<60 mV	0.035 + 0.02	0.045 + 0.02	0.1 + 0.02	0.17 + 0.02	0.002 + 0.0002
100 mA	$\pm 200 \text{ mA}$	<100 mV	0.04 + 0.02	0.07 + 0.02	0.1 + 0.02	0.1 + 0.02	0.001 + 0.0002
1 A	$\pm 2 \text{ A}$	<250 mV	0.07 + 0.02	0.4 + 0.02	0.9 + 0.02	1.6 + 0.02	0.002 + 0.0001
3 A	$\pm 4.2 \text{ A}$ ²⁵	<700 mV	0.08 + 0.02	0.41 + 0.02	0.9 + 0.02	1.6 + 0.02	0.002 + 0.0001

AC Current General Specifications

Overrange	105% of range except 3 A
-----------	--------------------------

Diode Test Specifications

Range	10 V
Test current ²⁶	1 μA , 10 μA , 100 μA , 1 mA ²⁷
Accuracy	Add 20 ppm of reading to 10 VDC voltage specifications.

22. Specification typical above 5 kHz

23. Specification typical above 5 kHz

24. Applies to signals > 9 μArms

25. Sine wave only.

26. -13% to 0% tolerance, typical.

27. Up to 4.5 V measurement for 1 mA test current.

Frequency and Period



Note Aperture time set to 150 ms.

Frequency measurement range	15 Hz to 500 kHz
Period measurement range	2 μ s to 66.67 ms

Frequency input voltage range	Corresponding digitizer range ²⁸	Minimum peak-to-peak signal amplitude ²⁹	Maximum peak-to-peak signal amplitude	Accuracy
50 mV	100 mV	5 mV	200 mV	Refer to the PXIe_CLK100 accuracy of the chassis.
500 mV	1 V	50 mV	2 V	
5 V	10 V	500 mV	20 V	
50 V	100 V	5 V	200 V	
700 V	1000 V	50 V	1000 V	

Temperature Specifications

All temperature accuracy specifications apply to apertures ≥ 100 ms, Auto Zero, and ADC calibration enabled. Use the lowest possible resistance or voltage range for each temperature. Add probe accuracy and cold junction accuracy where applicable.

Sensor type	Temperature range	Accuracy
RTD ³⁰	-200 °C to 600 °C	0.1 °C
Thermistor ³¹	-80 °C to 150 °C	0.08 °C
J Thermocouple	-210 °C to 1200 °C	0.2 °C

28. AC Coupled.

29. Square wave input. Minimum required peak-to-peak signal level is valid only for frequencies up to the -3 dB bandwidth. For higher frequencies, the signal amplitude must be increased. Refer to the Digitizer Voltage Mode for bandwidths.

30. Based on Pt3851 RTD in a 4-wire configuration.

31. Based on 44004, 44006, and 44007 interchangeable thermistors.

Sensor type	Temperature range	Accuracy
K Thermocouple	-200 °C to 1200 °C	0.3 °C
N Thermocouple	-200 °C to 1300 °C	0.4 °C
T Thermocouple	-200 °C to 400 °C	0.3 °C
E Thermocouple	-200 °C to 1000 °C	0.2 °C
R Thermocouple	-50 °C to 1760 °C	0.8 °C
S Thermocouple	-50 °C to 1760 °C	0.8 °C
B Thermocouple	400 °C to 1820 °C	0.8 °C

Isolated Digitizer Specifications

Available functions	Voltage and current
Voltage ranges	±100 mV to ±1000 V (DC or AC coupled)
Current ranges	100 µA to 3 A
Sample rate range	10 S/s to 1.8 MS/s
Available sample rates	$r = (1.8 \text{ MS/s}) / y$, where $y = 1, 2, 3, \dots, 1.8 \times 10^5$
Timebase accuracy	Equal to the PXIe_CLK100 accuracy of the chassis
Digitizer record length	2 samples minimum, unlimited maximum

Table 8. Voltage Mode

Range	Input resistance ³²	DC accuracy (ppm/reading + ppm/range) 2 year, T _{selfcal} ±5 °C	Analog bandwidth ³³ , typical	
			±0.1 dB	-3 dB
100 mV	10 MΩ ± 2%, >10 GΩ	125 + 175	60 kHz	300 kHz
1 V		125 + 75	50 kHz	300 kHz
10 V		125 + 75	50 kHz	300 kHz
100 V	10 MΩ ± 2%	125 + 75	20 kHz	250 kHz

32. In parallel with 90 pF. When AC coupled, only 10 MΩ available.

33. Typical AC coupled frequency is 6 Hz (+/- 0.1 dB) and 0.8 Hz (-3 dB).

Range	Input resistance	DC accuracy (ppm/reading + ppm/range) 2 year, $T_{selfcal} \pm 5^\circ\text{C}$	Analog bandwidth, typical	
			$\pm 0.1\text{ dB}$	-3 dB
1000 V		125 + 75	30 kHz	275 kHz

Figure 5. PXle-4081 Voltage Waveform Noise with Input Shorted, Typical

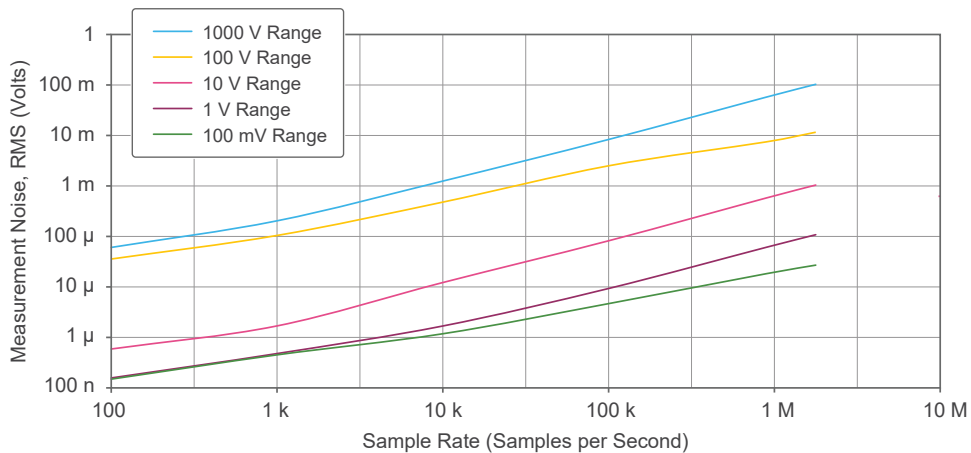
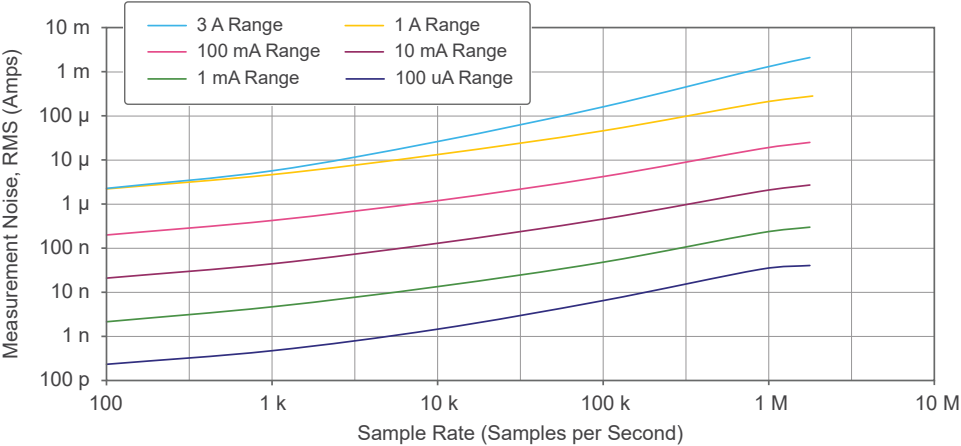


Table 9. Current Mode

Range	Burden voltage, typical	DC accuracy (ppm/reading + ppm/range) 2 Year, $T_{selfcal} \pm 5^\circ\text{C}$	Analog bandwidth, typical	
			$\pm 0.1\text{ dB}$	-3 dB
100 μA	<60 mV	230 + 75	50 kHz	350 kHz
1 mA	<60 mV	230 + 75	60 kHz	400 kHz
10 mA	<60 mV	265 + 75	70 kHz	400 kHz
100 mA	<100 mV	265 + 75	80 kHz	400 kHz
1 A	<250 mV	800 + 75	10 kHz	450 kHz
3 A	<700 mV	800 + 75	10 kHz	450 kHz

Figure 6. PXIe-4081 Current Waveform Noise with Input Open, Typical



General Specifications

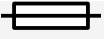
External calibration interval	2 years
Warm-up	60 minutes to rated accuracy
Measurement Category	I (up to 1000 VDC, 700 VAC _{rms} , 1000 V _{pk}) II (up to 250 VAC _{rms} , 220 VDC)

Caution Do not connect the product to signals or use for measurements within Measurement Categories III or IV.

Input protection (between terminals or terminal to ground)	1000 VDC or V _{pk}
Current mode fuse	T 3.5 A 1000 V, time-lag Minimum interrupt rating: 10 kA Siba 5019906.3,5
Maximum common-mode voltage	500 VDC or VAC _{rms}

Maximum Voltage to Earth Ground	
HI	1000 VDC or V _{pk}

LO	500 VDC or VAC _{rms}
HI SENSE	500 VDC or VAC _{rms}
LO SENSE	500 VDC or VAC _{rms}



Fuse When this fuse symbol is marked on a device, take proper precautions.



Caution Take precautions to avoid electrical shock.

Timing

Mode	Trigger latency		Maximum reading rate ³⁴
	AC voltage	All functions except AC voltage ³⁵	
Voltage, current, and resistance	15 μ s	<0 μ s	20 kS/s
Voltage and current digitizer			1.8 MS/s

Power

Power consumption	<9 W from PXI Express backplane
+12 V load	0.55 A max
+3.3 V load	0.55 A max

Physical Characteristics

Dimensions	3U, one-slot, PXI/cPCI module
------------	-------------------------------

34. Maximum Reading Rate assumes minimum aperture time, Auto Zero is OFF, Offset Compensated Ohms is OFF, ADC Calibration is OFF, Number of Averages is 1, and Settle Time is 0 seconds. Varying these settings affects the reading rate.
35. Trigger latency for all functions except AC Voltage assumes Auto Zero, Offset Compensated Ohms, and ADC Calibration are OFF.

	2.0 cm x 13.0 cm x 21.6 cm (0.8 in. x 5.1 in. x 8.5 in.), nominal
Weight	340 g (12 oz), nominal



Note If you need to clean the device, wipe it with a dry towel.

Environment

Maximum altitude	2,000 m (800 mbar) (at 25 °C ambient temperature)
Pollution Degree	2

Indoor use only.

Operating Environment

Ambient temperature range	0 °C to 55 °C
Relative humidity range	10% to 90%, noncondensing

Storage Environment

Ambient temperature range	-40 °C to 71 °C
Relative humidity range	5% to 95%, noncondensing

Shock and Vibration

Operating shock	30 g peak, half-sine, 11 ms pulse
Random vibration	
Operating	5 Hz to 500 Hz, 0.3 g RMS
Nonoperating	5 Hz to 500 Hz, 2.4 g RMS